Gp 1104

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6

the application of:

2-2-9

et al

ial No.: 07/743,383

Group Art Unit: 1104

Filed: August 21, 1991

Examiner: G. Goudreau

For: METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND

APPARATUS USED IN SUCH METHOD

PETITION FOR EXTENSION OF TIME

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20006

January 7, 1993

Sir:

Applicant petitions the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated October 5, 1992, for one month from January 5, 1993, to February 5, 1993.

Please charge Deposit Account Number 14-1060 in the amount of One Hundred and Ten Dollars (\$110.00) to cover the cost of the extension. In the event that any additional fees are due with respect to this paper, please charge our Deposit Account No. 14-1060. A duplicate copy of this paper is enclosed herewith.

Respectfully submitted,

P 30203 01/21/93 07743383

Jyan Carlos A. Marque

Registration No. 3

Atty. Docket No. P698-1333

NIKAIDO, MARMELSTEIN, MURRAY & ORAM

Metropolitan Square 655 Fifteenth Street, N.W. Suite 330 - G Street Lobby Washington, D.C. 20005-5701 (202) 638-5000 JCM/myw

Enclosure: Duplicate copy of Petition